1	L Number	Hits	Search Text	DB	Time stamp
EPO, JPO, DCRWENT; IDM, TOB USPAT; US-CRUB; EPO, JPO; DCRWENT; IDM, TOB USPAT; US-CRUB; EPO; JPO; DCRWENT; I					
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S-PGPUB; EPP; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	2	14	714/45 47 ccls and cache same trigger	_	2004/10/15 12:49
2PC, JPC; DCRNENT; LBM_TDB	2	1 1	/14/45,47.ccis. and cache same trigger		2004/10/13 12:40
DERMENT;   IBM TOB   USPAT;				4	
3   259   714/45,47.ccls. and cache   USPAT; USPAT; USPACT;				1	
4		1		IBM_TDB	
A	3	259	714/45,47.ccls. and cache	· ·	2004/10/15 12:48
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Cache   same (data adj cache)	4	44	714/45,47.ccls. and (instruction adi	_	2004/10/15 12:48
DERMENT;				1	
5		!		EPO; JPO;	
5				DERWENT;	
Same trac\$3		_	714/45 47 1		0004/40/45 40 40
The content of the	5	5		1	2004/10/15 12:48
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or ("5274811") or ("5608720") or ("6148123") or ("6009270") or ("6145123") or ("6148181") or ("6202127") or ("61485295").PN.  145 (instruction adj cache) same (data adj cache) same trac\$3  2 714/45.ccls. and (instruction adj cache) same (data adj cache) same (data adj cache) same (data adj cache) same trac\$3  - 2 714/45.ccls. and (instruction adj cache) same (before the cache) same (data adj cache) same cPU same trac\$3  - 8 (instruction adj cache) same (data adj cache) same (data adj cache) same (data adj cache) same serial  - 195 parameters same (instruction adj register) same serial  - 8 parameters same (instruction adj register) same serial  - 74 (instruction adj cache) same (data adj cache) same prob\$3  - 74 (instruction adj cache) same (data adj cache) same prob\$3  - 10 (instruction adj cache) same (data adj cache) same (data adj cache) same prob\$3  - 11 (instruction adj cache) same (data adj cache) same (			•		
145	7	10			2004/10/15 12:50
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Cache) same trac\$3		1.45		HEDAT.	2002/00/11 14.54
Pop		143			2003/09/11 14:54
-   2   714/45.ccls. and (instruction adj cache)   DERWENT; IBM_TDB   USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB   USPAT; USPA		•	cache, same cracys	1	
2   714/45.ccls. and (instruction adj cache)   USPĀT;   US-PGPUB;   EPO; JPO;   DERWENT;   IBM_TDB   USPĀT;					
Same (data adj cache) same trac\$3					
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Tool	1		same (data adj cache) same trac\$3	i ·	
TBM_TDB				ī	
Total				1	
EPO; JPO; DERWENT; IBM_TDB   2003/09/12 15:37   US-PGPUB; EPO; JPO; DERWENT; IBM_TDB   USPĀT; US-PGPUB; EPO; JPO	_	750	(instruction adj cache) same (data adj		2003/09/12 14:05
DERWENT; IBM TDB USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPĀT; IBM TDB			cache) same CPU	US-PGPUB;	
TIM_TDB USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	İ	ļ			
Total					
cache) same CPU same trac\$3  US-PGPUB; EPO; JPO; DERWENT; IBM_TDB US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB US-PGPUB; EPO; JPO; DERWENT; IBM_TDB US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	_	R	(instruction adj cache) same (data adj		2003/09/12 15:37
Parameters same (instruction adj register)  195  parameters same (instruction adj register)  8 parameters same (instruction adj register)  8 parameters same (instruction adj register)  9 same serial  10 parameters same (instruction adj register)  11 (instruction adj cache) same (data adj uspāt;  11 (instruction adj cache) same (data adj uspāt;  12 cache) same prob\$3  13 (instruction adj cache) same (data adj uspāt;  14 (instruction adj cache) same (data adj uspāt;  15 parameters same (instruction adj register)  16 uspāt;  17 uspāt;  18 uspāt;  2003/09/12 15:48					2003/03/12 13.3/
- 195 parameters same (instruction adj register)  - 195 parameters same (instruction adj register)  - 8 parameters same (instruction adj register)  - 8 parameters same (instruction adj register)  - 10 same serial  - 11 (instruction adj cache) same (data adj cache)  - 11 (instruction adj cache) same (data adj cache)  - 11 (instruction adj cache) same (data adj cache)  - 11 (instruction adj cache) same (data adj cache)  - 11 (instruction adj cache) same (data adj cache)  - 11 (instruction adj cache) same (data adj cache)  - 12 coo3/09/12 15:02  - 2003/09/12 15:02  - 2003/09/12 15:02					-
- 195 parameters same (instruction adj register) USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPĀT;				DERWENT;	
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; IBM_TDB USPAT					0000/00/55 55 55
Parameters same (instruction adj register)  apparameters same (instruction adj register)  Berwent;  IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;  IBM_TDB USPAT; IBM_TDB USPAT; IBM_TDB USPAT; IBM_TDB USPAT; IBM_TDB USPAT; IBM_TDB USPAT; IBM_TDB USPAT; IBM_TDB USPAT;	-	195	parameters same (instruction adj register)	·	2003/09/12 15:02
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;					
TBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; USP					
parameters same (instruction adj register) USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; U				1	
The struction adj cache) same (data adj	-	8			2003/09/12 15:02
The struction adj cache) same (data adj cache) same (data adj cache) same prob\$3  The struction adj cache) same (data adj cache) sam			same serial		
- 74 (instruction adj cache) same (data adj USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB  (instruction adj cache) same (data adj USPAT; 2003/09/12 15:39					
- 74 (instruction adj cache) same (data adj USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPĀT; 2003/09/12 15:48				1	
cache) same prob\$3  US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; 2003/09/12 15:48	_	74	(instruction adj cache) same (data adj		2003/09/12 15:39
EPO; JPO; DERWENT; IBM_TDB USPAT; 2003/09/12 15:48			' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '		111, 31, 11
IBM_TDB   USPAT; 2003/09/12 15:48			-		
- 11 (instruction adj cache) same (data adj USPAT; 2003/09/12 15:48				•	
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-	750	(instruction adj cache) same (data adj	USPAT;	2003/09/12 15:48
		cache) same CPU	US-PGPUB;	
			EPO; JPO;	
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			IBM_TDB	
-	536	CPU same cache same trac\$3	USPAT;	2003/09/12 15:50
			US-PGPUB;	
ļ			EPO; JPO;	
			DERWENT;	
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-	7	714/45,47.ccls. and (CPU same cache same	USPAT;	2003/09/12 17:52
		trac\$3)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
i			IBM TDB	
l _	72	714/45,47.ccls. and trac\$3 same (prior or	USPĀT;	2003/09/12 17:57
		before or pursuant or preced\$3) same	US-PGPUB;	2003,03,12 17.37
		(subsequent or after or follow\$3)	EPO; JPO;	
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			EPO; JPO;	
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	43536	"912"	IBM_TDB	0000/00/15 16 15
-	43536		USPAT;	2003/09/15 16:15
			US-PGPUB;	
			EPO; JPO;	
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-	1	6618775.pn. and "912"	USPAT;	2003/09/16 07:59
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
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	1	6618775.pn.	USPAT	2004/10/14 18:26